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501.37959CC2

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicants: Makoto YOSHIDA et al.  
Serial No.: 10/653,889  
Filed: September 4, 2003  
For: **SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE AND  
MANUFACTURING METHOD OF MINIATURIZATION OF  
MEMORY CELLS WITH REDUCED BIT LINES (As Amended)**  
Group: 2814  
Examiner: Hoai V. Pham

**AMENDMENT**

**Mail Stop Amendment**  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**November 30, 2004**

Sir:

In response to the Office Action (Paper No. 08062004) dated on August 10, 2004, please amend the above-identified application as follows.

**Amendments to the Claims** begin on page 2 of this paper.

**Remarks/Arguments** begin on page 7 of this paper.